



IN THE UNITED STATES PATENT & TRADEMARK OFFICE

Application Number : 10/731,482  
Applicant(s) : Joon Hyeon LEE  
Filed : December 10, 2003  
Tech Cntr/AU : 2812  
Examiner : Gurley, Lynne Ann  
Entitled : METHOD OF FORMING METAL LINE LAYER IN  
SEMICONDUCTOR DEVICE  
  
Attorney Reference : 123034-05004728  
Customer Number : 43569

**MAIL STOP RCE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

**PRELIMINARY AMENDMENT**

Sir/Madam:

Prior to initial examination, please amend the above-identified application as follows: